

Electronic Acknowledgement Receipt

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International Application Number:	
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Title of Invention:	HIGH SPEED POWER-GATING TECHNIQUE FOR INTEGRATED CIRCUIT DEVICES INCORPORATING A SLEEP MODE OF OPERATION
First Named Inventor/Applicant Name:	Kim C. Hardee
Customer Number:	25235
Filer:	William J. Kubida/Julie Lange
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		DOC060.PDF	33213 22d0c79c32eb7ce82bb247d1b119849af1516e7	yes	4

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Letter to Official Draftsman Filed P/E	1	1
	Drawings-only black and white line drawings	2	4
Warnings:			
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